

Diagram 10 illustrates a network architecture. At the top, a **CORE SWITCH** (18) is connected to five **ETHERNET BRIDGE** (12) units arranged in a mesh. A **BANDWIDTH MANAGER** (16) is connected to the top-right Ethernet bridge. Each of the five Ethernet bridges is connected to a **BANDWIDTH ALLOCATION INTERFACE** (14). These interfaces are further connected to **DATA SOURCE** (24) units. Specifically, the leftmost and rightmost interfaces connect to one data source each, the top-right interface connects to one data source, and the bottom two interfaces each connect to two data sources.

**FIG. 1**

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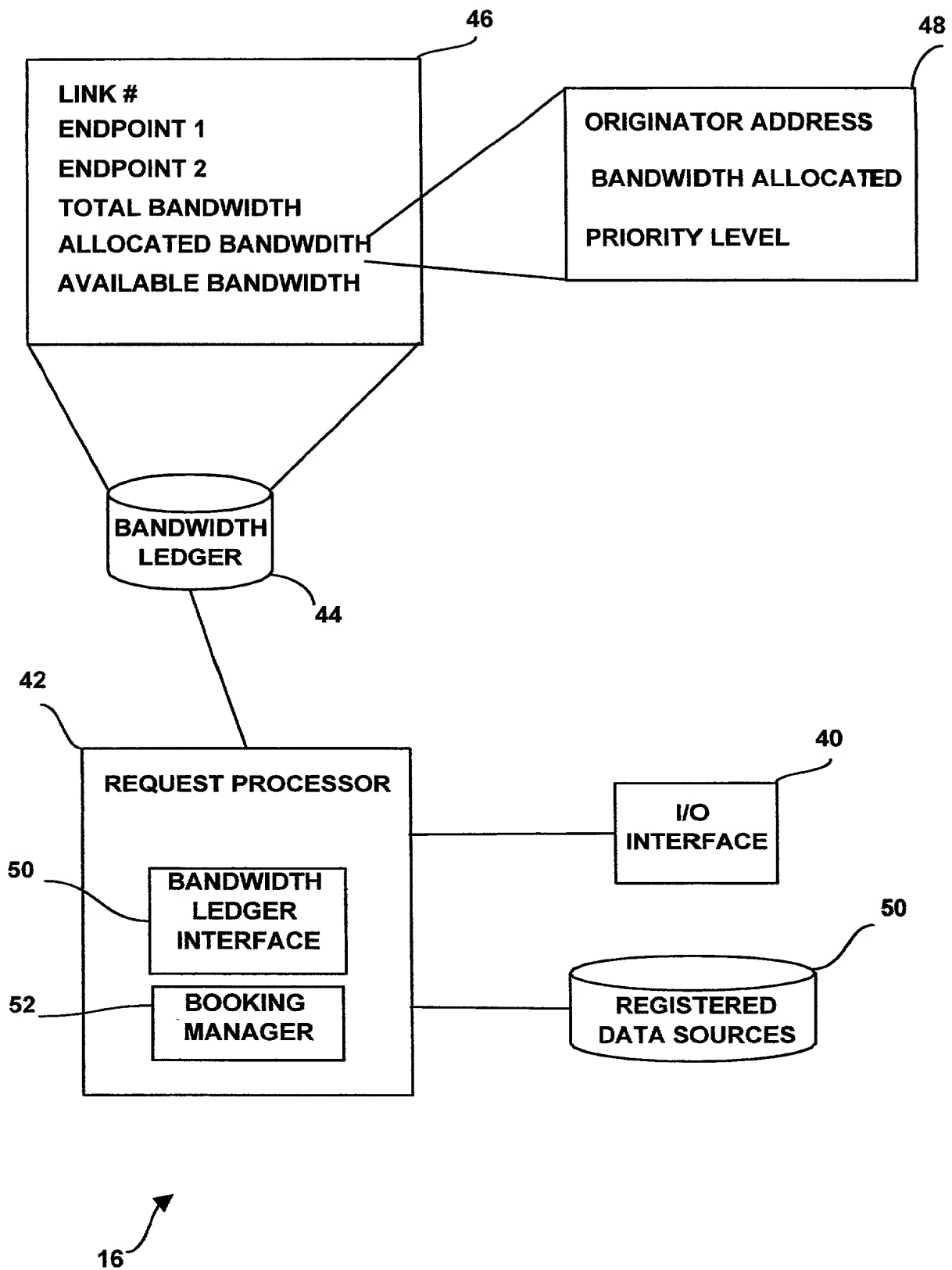
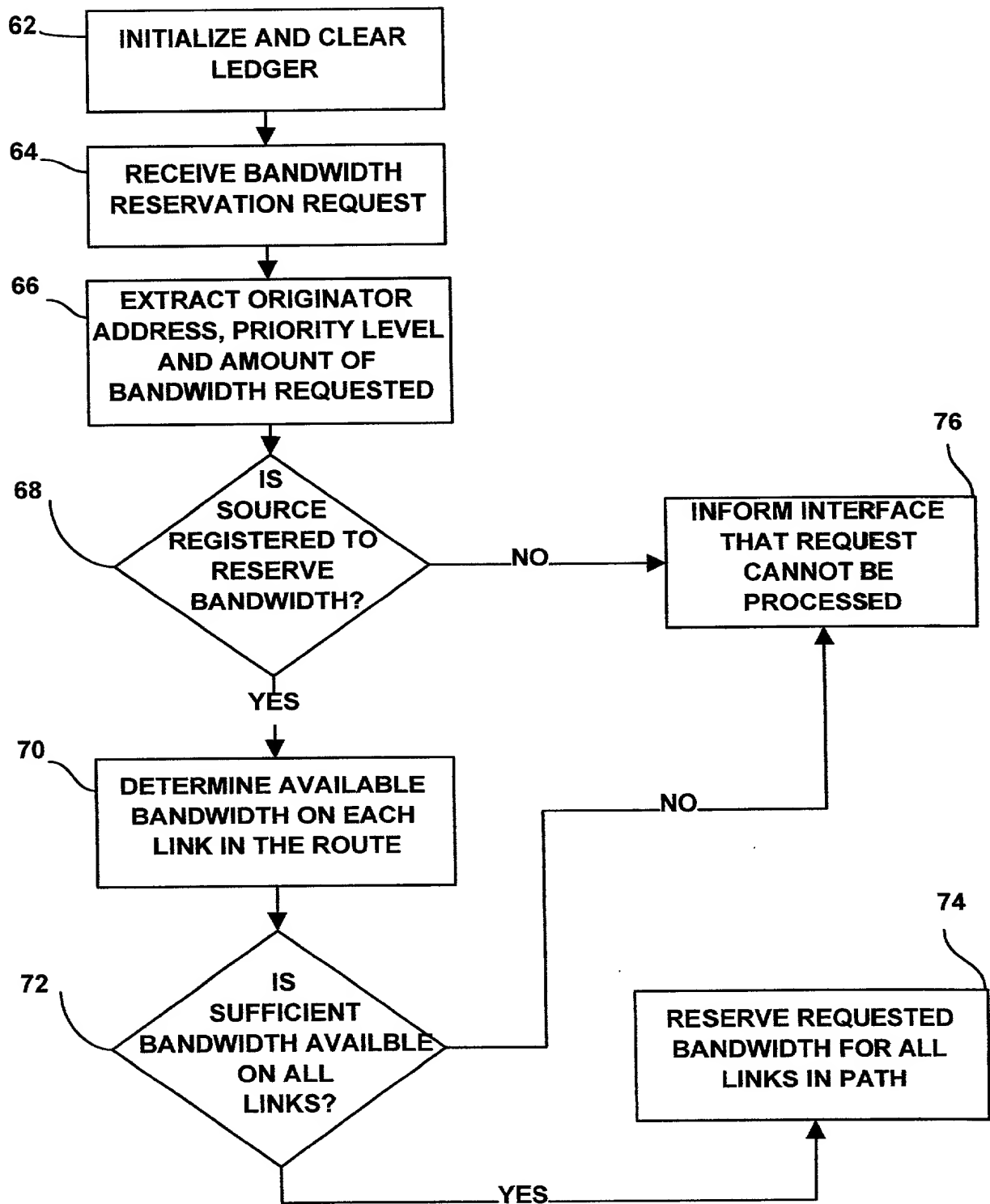
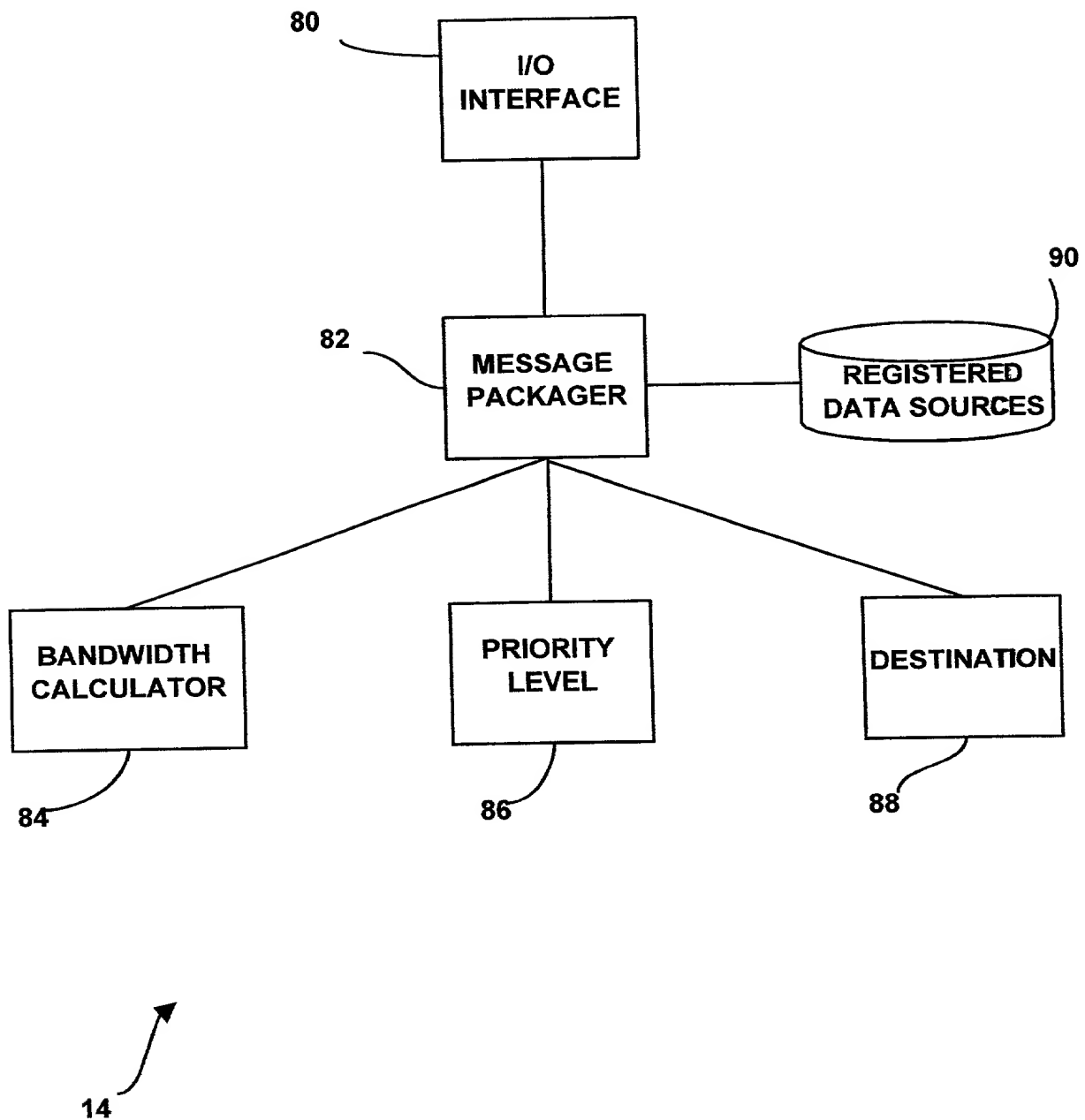


FIG. 3



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FIG. 4



**FIG. 5**